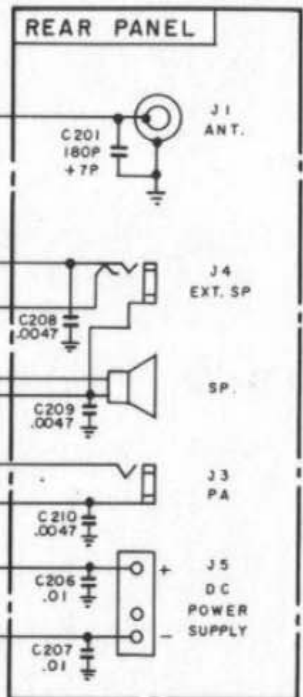
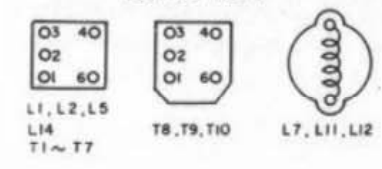


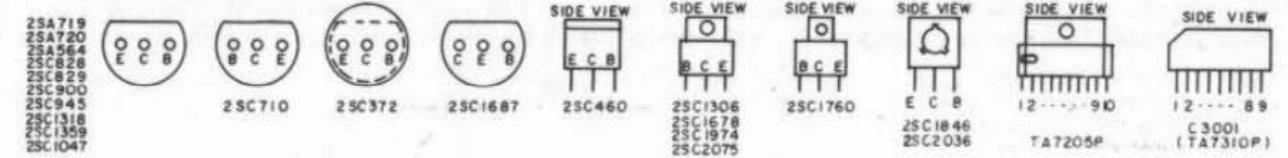
NOTE
 Δ VOLTS AT TRANSMIT CONDITION
 ALL VOLTAGES MEASURED FROM PC BOARD
 GND WITH D.C. VTVM AT NO SIGNAL
 (AT 13.8V POWER SUPPLY) IF MEAS-
 UREMENT VALUES OBTAINED ARE IN
 EXCESS OF ±20% OF VALUES SHOWN
 THEN REASON FOR DIFFERENCE SHOULD
 BE CORRECTED
 ⊥ CHASSIS GND ⊥ PC BOARD GND
 * VARIABLE



TRANSFORMER TERMINATION INFORMATION
 (BOTTOM VIEW)



SEMICONDUCTOR TERMINAL CONNECTION (BOTTOM VIEW UNLESS OTHERWISE NOTED)



E: EMITTER
 C: COLLECTOR
 B: BASE